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| APPLICATION NO.                | FILING DATE     | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO.     | CONFIRMATION NO. |
|--------------------------------|-----------------|----------------------|-------------------------|------------------|
| 10/708,415                     | 03/02/2004      | Amit PREMY           | TI-36317                | 2414             |
| 23494                          | 7590 12/27/2004 |                      | EXAMINER                |                  |
| TEXAS INSTRUMENTS INCORPORATED |                 |                      | JEAN PIERRE, PEGUY      |                  |
| P O BOX 65:                    | 5474, M/S 3999  |                      |                         |                  |
| DALLAS, TX 75265               |                 |                      | ART UNIT                | PAPER NUMBER     |
| ·                              |                 |                      | 2819                    |                  |
|                                |                 |                      | DATE MAILED: 12/27/2004 |                  |

Please find below and/or attached an Office communication concerning this application or proceeding.

|   | Application No.  | Applicant(s)  |                                       |  |  |  |
|---|--|---|---------------------------------------|--|--|--|
|   |  |   |                                       |  |  |  |
| Office Action Summan  | 10/708,415   | PREMY ET AL.  |                                       |  |  |  |
| Office Action Summary   | Examiner   | Art Unit  | ليهم                                  |  |  |  |
|   | Peguy JeanPierre   | 2819  | · · · · · · · · · · · · · · · · · · · |  |  |  |
| The MAILING DATE of this communication ap<br>Period for Reply   | ppears on the cover sheet with the c   | correspondence ad   | dress                                 |  |  |  |
| A SHORTENED STATUTORY PERIOD FOR REP THE MAILING DATE OF THIS COMMUNICATION  - Extensions of time may be available under the provisions of 37 CFR 1 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a re  - If NO period for reply is specified above, the maximum statutory perior  - Failure to reply within the set or extended period for reply will, by statu Any reply received by the Office later than three months after the mail earned patent term adjustment. See 37 CFR 1.704(b).  | . 136(a). In no event, however, may a reply be tin ply within the statutory minimum of thirty (30) day d will apply and will expire SIX (6) MONTHS from te, cause the application to become ABANDONE | nely filed<br>s will be considered timely<br>the mailing date of this co<br>D (35 U.S.C.§ 133). | /.<br>mmunication.                    |  |  |  |
| Status  |  |   |                                       |  |  |  |
| 1)⊠ Responsive to communication(s) filed on 02.   | March 2004.  |   |                                       |  |  |  |
|   | is action is non-final.  |   |                                       |  |  |  |
| •   |  |   |                                       |  |  |  |
| Disposition of Claims   |  |   |                                       |  |  |  |
| 4) ⊠ Claim(s) <u>1-26</u> is/are pending in the application 4a) Of the above claim(s) is/are withdrest 5) ⊠ Claim(s) <u>5-12</u> is/are allowed.  6) ⊠ Claim(s) <u>1-4, 13,19 and 23</u> is/are rejected.  7) ⊠ Claim(s) <u>14-18,20-22 and 24-26</u> is/are object solution and the subject to restriction and the subject to restrict the subject to restriction and the subject to restrict the subject to re | awn from consideration.  |   |                                       |  |  |  |
| Application Papers  |  |   | ·                                     |  |  |  |
| 9) The specification is objected to by the Examir 10) The drawing(s) filed on is/are: a) according to the Applicant may not request that any objection to the Replacement drawing sheet(s) including the corre  | ccepted or b) objected to by the lead of a because of common or by the lead in abeyance. See   | e 37 CFR 1.85(a).   | FR 1 121(d)                           |  |  |  |
| 11) The oath or declaration is objected to by the E   | •  | =   | • •                                   |  |  |  |
| Priority under 35 U.S.C. § 119  |  |   |                                       |  |  |  |
| 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Bure * See the attached detailed Office action for a list.   | nts have been received.<br>nts have been received in Applicati<br>ority documents have been receive<br>au (PCT Rule 17.2(a)).  | ion No<br>ed in this National   | Stage                                 |  |  |  |
| Attachment(s)   | Δ□ 1   | VDTO 442)   |                                       |  |  |  |
| 1) Motice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)   | 4)  Interview Summary<br>Paper No(s)/Mail D  |   |                                       |  |  |  |
| 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date   |  |   | )-152)                                |  |  |  |

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### **DETAILED ACTION**

# Claim Rejections - 35 USC § 112

1. Claims 1-4 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 1, lines 13-14, the term "wherein the cost... in the form of said die." is confusing. The preamble recites a method of testing an integrated circuit. The limitations cited above, have nothing to do with the testing of the integrated circuit. It is not clear what steps contribute to minimize the testing method. Please clarify.

2. It is suggested n claim 4, line 1, that the article "the" be added before --method--.

#### Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 4. Claims 13, 19, 23 are rejected under 35 U.S.C. 102(b) as being anticipated by Sunter et al. (USP 5,659,312).

Sunter et al. disclose a method of testing a mixed integrated circuit (see abstract). The system comprises a built-in-test-control (BIST) (40), a DAC (20), an ADC (14). The ADC (14) and the analog circuits (34, 10, 28,36 24) represent a receiver block that receives the analog signal. The analog signals are being generated externally after converting

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the digital data elements (N1) by the DAC (20). The mixed integrated circuit further comprises a computation block (16 Fig.1; (44,40 Fig. 3) (see col. 4, lines 44-46) and is configured to measure a deviation as shown in Figure 4 from a signal level that corresponds to the digital data and make a decision on whether to discard/qualify the signal based on the deviation (see col. 6, lines 39-47).

## Allowable Subject Matter

- 5. Claims 1-4 would be allowable if rewritten or amended to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action.
- 6. Claims 5-12 are allowed.
- 7. Claims 14-18, 20-22, 24-26 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 8. The following is a statement of reasons for the indication of allowable subject matter: The prior art of record does not teach a connection between an integrated circuit and a calibrated integrated circuit in which the calibrated integrated circuit examines a mixed signal to determine whether to discard/qualify the signal; the mixed signal integrated circuit further operates with a plurality of valid symbols having a plurality of vector components contained in a constellation table and also comprises a plurality of DAC to generate a plurality of base band signals, phase shifted with respect to one another by converting corresponding vector components, a plurality of upconversion mixers that generate modulated signal, an adder to add the modulated signals to generate an analog signal.

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9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Peguy JeanPierre whose telephone number is (571) 272-1803. The examiner fax phone number is (571) 273-1803.

Peduy JeanPierre Primary Examiner